


ABSTRACT OF THE DISCLOSURE

 An electronic imaging system includes a CCD image sensor arrangement having a two dimensional array of pixels. The pixel array converts light incident thereon to an electric signal. The pixels are arranged in a plurality of horizontal rows or lines, the lines being arranged vertically. A control arrangement selectively controls operational modes of the system. One mode provides for sequential scan reading out of pixel signals for entire lines of the CCD for still picture recording. Another mode provides for reading out pixel signals concerning k (k is a positive integer) vertically continuous lines of the CCD for still picture recording or dynamic image processing. Thus it is possible to provide a high pixel density solid-state image sensor output at a high frame rate without use of any high drive frequency.